

JUNE 26TH, 2025 11AM ET

Exploring FPGA Design: Practical Tips for Scaling Between FPGA Families

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Join the conversation shaping the future of FPGA technology!

Join us for an exclusive webinar in collaboration with Altera and Embedded Computing Design, where industry leaders will share real-world strategies for scaling FPGA designs across device families.

 - **June 26th, 2025** - **11AM ET**

Whether you're new to FPGAs or an experienced designer, this session will help broaden your understanding of FPGA design considerations and how scaling occurs between Agilex™ 3 and Agilex™ 5 families. Together, we'll explore real-world lessons from designing with Altera's Agilex™ 3 and Agilex™ 5 FPGAs, and how common IP, workflows, and toolchains can simplify your design journey.

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Featured Speakers

**Lux Joshi**

Sr. Director, Silicon Product
Management & Marketing, Altera

**Liz Chen**

Sr. Managing Director,
Terasic Inc

**Tawfeeq Ahmad**

Associate Director,
Product Marketing, iWave

**Ken Briodagh**

Senior Technology Editor,
Embedded Computing Design

Discover more resources with regards to the Agilex Boards and Solutions from iWave

[Agilex™ 3 SoM Overview Video](#)

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[Agilex™ 5 SoM Overview Video](#)

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